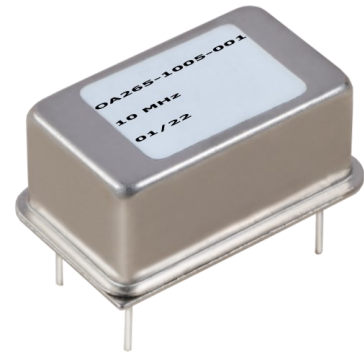


## OCXO Model: OA265-1005-001

Issue 1; 7th May 2024

### Features

- Temperature stability to  $\pm 100$ ppb
- Low phase noise options
- Frequency range 10.0MHz
- Low pre-aged options available
- Industry standard package
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements



### Specification

- Temperature stability:  $\pm 100$ ppb over  $(-40$  to  $70)^{\circ}\text{C}$
- Output: CMOS 15pF, 45% 55%
- Voltage: 3.3V
- Warm up Current: 560mA
- Quiescent current: 240mA

### Phase Noise (typical)

- $F_{0}+10\text{Hz}$  -115 dBc/Hz
- $F_{0}+100\text{Hz}$  -140 dBc/Hz
- $F_{0}+1\text{KHz}$  -152 dBc/Hz
- $F_{0}+10\text{KHz}$  -160 dBc/Hz
- $F_{0}+100\text{KHz}$  -165 dBc/Hz

### Voltage /Load change

- $\pm 5\%$  supply voltage change:  $\pm 2$ ppb
- $\pm 10\%$  load change:  $\pm 2$ ppb

### Ageing

Based on 10MHz unit after 30 days continuous operation:

- Per day:  $\pm 2$ ppb max.
- Per year:  $\pm 500$ ppb max.
- Warm up time: 1 minute max. to within  $\pm 0.25$ ppm of nominal

### Voltage Trim

- $\pm 10$ ppm typical
- Trim impedance 50K $\Omega$

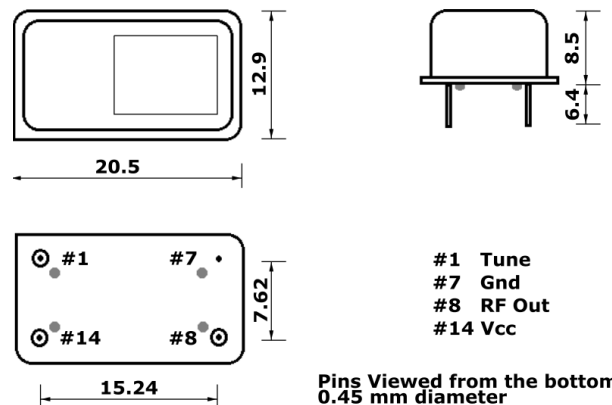
### Reference Options

- N/A

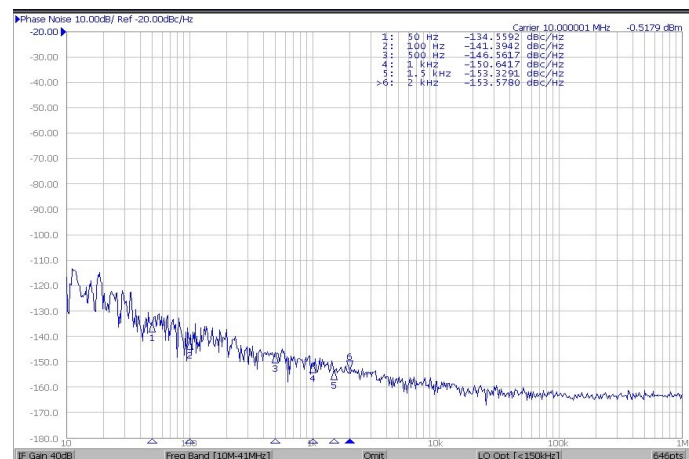
### Environmental

- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range:  $(-55$  to  $125)^{\circ}\text{C}$
- Mechanical shock: MIL standard 202F, method 213, condition J

### Dimensions (mm)



### Phase Noise Plot



- Thermal shock: MIL standard 202F, method 107, condition A
- Vibration: MIL standard 202F, method 204, condition B
- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C

#### Compliance

- RoHS Status (2011/65/EU) - Compliant
- REACH Status - Compliant

#### Packaging

- Pack Style: Bulk

#### Ordering Information

- Unique customer part number and custom specification issued with each application
- OCXO part number: OA265-1005-001
- Frequency: 10.0MHz

#### Test Circuit - CMOS Load

